



AF/2811X

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Katsumi Yamaguchi
Serial No.: 10/087,556
Filed: 03/01/2002
For: Semiconductor Device and Bump Formation Method

TI No.: 31471
Art Unit: 2811
Examiner: Im, Junghwa M.
Conf. No.: 2191

#70
get.1m8.
J. McMillan
8/4/03

EXTENSION OF TIME

Commissioner For Patents
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

[Signature] 7/14/2003
Elizabeth Austin Date

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

<input checked="" type="checkbox"/>	One Month	(\$110)	<input type="checkbox"/>	Four Months	(\$1,450)
<input type="checkbox"/>	Two Months	(\$410)	<input type="checkbox"/>	Five Months	(\$1,970)
<input type="checkbox"/>	Three Months	(\$930)			

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

07/22/2003 BABRAHA1 00000012 200668 10087556
01 FC:1251 110.00 DA

Respectfully submitted,

[Signature]
Michael K. Skrehot
Registration No. 36,682

TECH
06/29/2003
RECEIVED
06/29/2003
U.S. PATENT AND TRADEMARK OFFICE

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